

Logic Probe

Parameter	Model	SPL2016	SLA1016
			
Input Channels		16	16
Input Impedance		100k Ω 18pF	100k Ω 8pF
Maximum Input Voltage		$\pm$ 50V Peak	$\pm$ 20V Peak
Input Dynamic Range		$\pm$ 20V	$\pm$ 10V
User defined threshold range		-10V ~ 10V (10mV steps)	-8V ~ 8V (10mV steps)
Threshold Selections		TTL(1.5V)、CMOS(2.5V)、 3.3V_LVCMOS(1.65V)、 2.5V_LVCMOS(1.25V)	TTL(1.5V)、CMOS(2.5V)、 3.3V_LVCMOS(1.65V)、 2.5V_LVCMOS(1.25V)
Threshold Accuracy		$\pm$ (3% of threshold setting +200mV)	$\pm$ (3% of threshold setting +150mV)
Threshold Groupings		Group 2: D15-D8	Group 2: D15-D8
		Group 1: D7-D0	Group 1: D7-D0
Minimum Input Voltage Swing		800mVpp	800mVpp
Maximum Input Data Rate		300 Mbps	120 Mbps
Minimum Detectable Pulse Width		3.3ns	8.3ns
Channel-to-Channel Skew		$\pm$ 1 digital sample interval	$\pm$ 1 digital sample interval