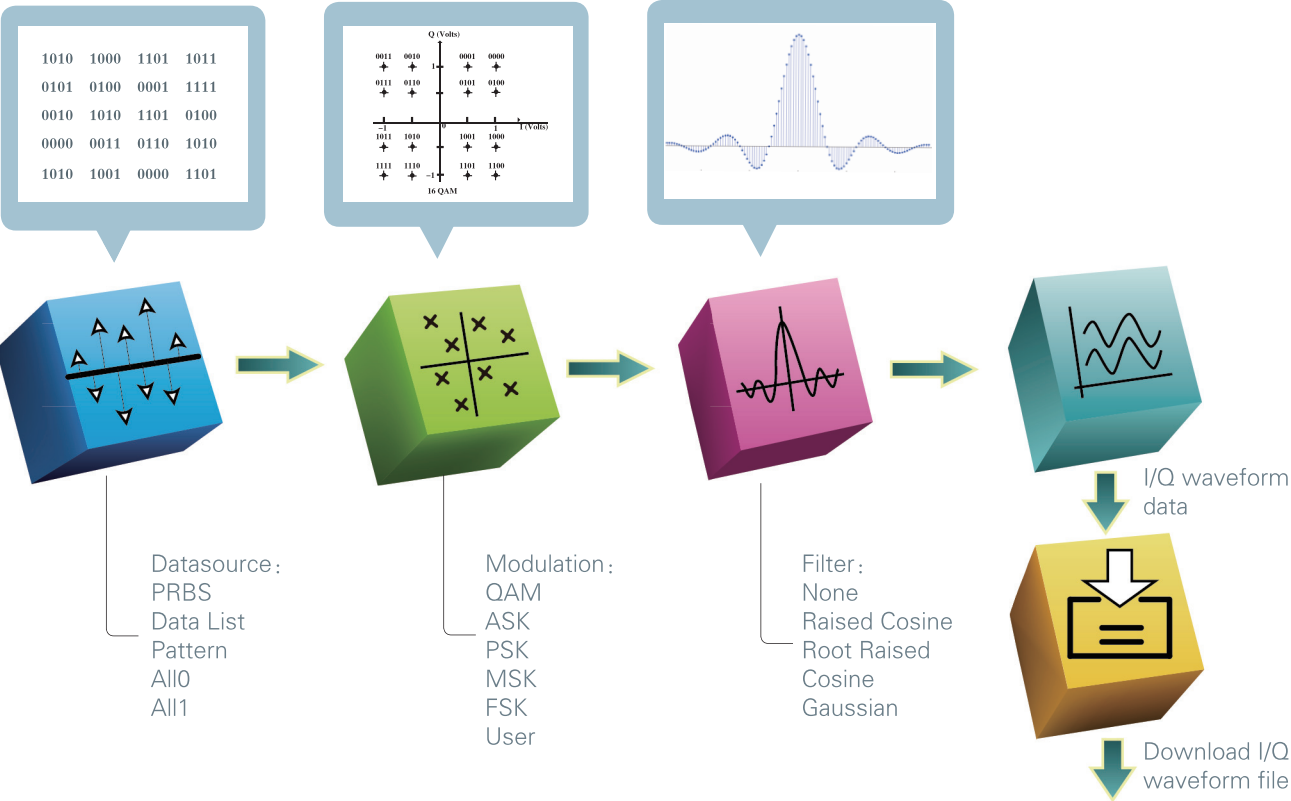




IQ Modulation Data Sheet

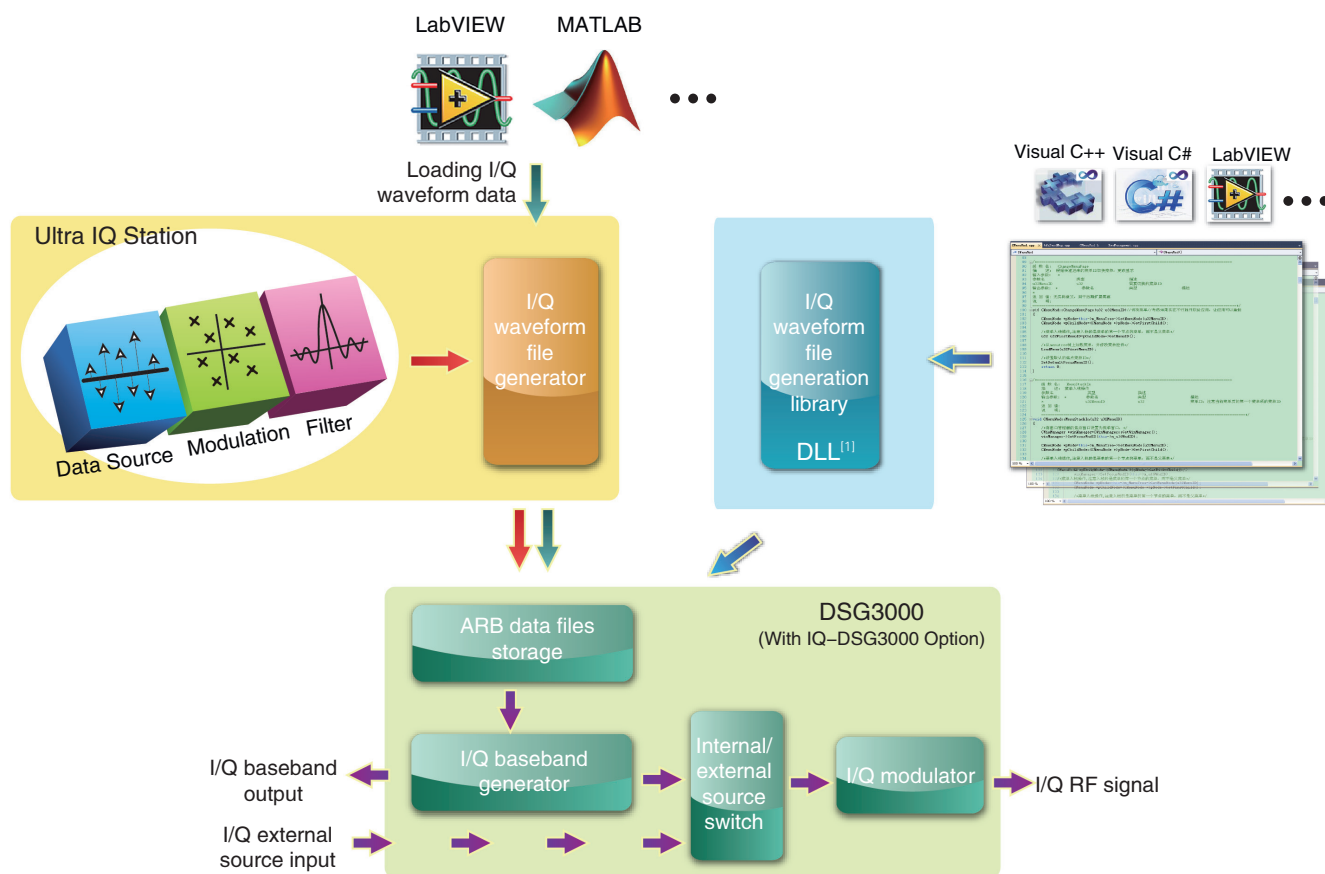
Ultra IQ Station PC Software
DSG3000 Series RF Signal Generator
IQ-DSG3000 Option



Product Features

- Provides wide variety of data source types, including PRBS, Data List, Pattern, All0 and All1
- The modulation mode includes QAM, ASK, PSK, FSK, MSK and User
- Provides rectangular filter, raised cosine filter, root raised cosine filter, and Gaussian filter
- Diverse waveform display modes: $i(t)/q(t)$, FFT Magnitude and Constellation; waveform zooming in or out function
- Supports three kinds of ARB file download methods: use Ultra IQ Station software to edit and download, load the ARB file edited by other software using Ultra IQ Station software, program by recalling function to edit and download
- Supplement and save the external ARB data
- Ease-to-use graphical user interface
- IQ modulation which supports internal and external modulation modes and IQ baseband output

I/Q Signal Generation and Download (Three Methods)



- ➡ The ARB file is edited and downloaded into DSG3000 by the Ultra IQ Station PC software.
- ➡ The ARB file edited by other software (e.g. MATLAB, LabVIEW, MathType and so on) is loaded into DSG3000 using the Ultra IQ Station PC software.
- ➡ The ARB file is generated and downloaded into DSG3000 under Visual C++, Visual C# and LabVIEW environment on the basis of I/Q dynamic link library.

NOTE: [1] For more information about IQ dynamic link library and IQ Modulation Programming Guide, please visit www.rigol.com to download.

► Specifications (Ultra IQ Station PC Software)

Data Source	
Data source type	PRBS, Data List, Pattern, All0, All1
Data source length	1~2M symbols
Symbol rate	100 ~10M syms/s
Coding mode	Default, Gray

Modulation Mode	
QAM	16QAM, 32QAM, 64QAM, 128QAM, 256QAM
ASK	2ASK, 4ASK, 8ASK, 16ASK, 32ASK
PSK	BPSK, QPSK, $\pi/4$ -QPSK, $\pi/4$ -DQPSK, 8PSK
FSK	MSK, 2FSK, 4FSK
User	The *.map file defined by user ^[1]

Filter	
Filter type	Raised Cosine, Root Raised Cosine, Gaussian
Roll of factor	0.05~1.00 (Raised Cosine and Root Raised Cosine)
B*T (Bandwidth*PatternPeriod)	0.15~1000.00 (Gaussian)
Impulse length	1~128 symbols
Oversampling	2~32

Graph View	
Display mode	i(t)/q(t), FFT Magnitude, Constellation
FFT length	128, 256, 512, 1024, 2048, 4096, 8192, 16384, 32768, 65536
Window function type	Rectangle, Hanning, Flat Top, Blackman

NOTE: [1] The *.map file loaded must comply with the format of the constellation graph corresponding to the QAM modulation type (such as 16QAM, 32QAM, 64QAM, 128QAM or 256QAM).

► Appendix (IQ-DSG3000 Option)

I/Q Modulation (Option IQ-DSG3000)		
Modulation source	External, internal	
Bandwidth (RF)	External modulation	
	Baseband (I or Q)	$\leq 120\text{MHz (nom.)}$
	RF (I + Q)	$\leq 240\text{MHz (nom.)}$
	Internal modulation	
	Baseband (I or Q)	$\leq 30\text{MHz (nom.)}$
	RF (I + Q)	$\leq 60\text{MHz (nom.)}$
Carrier suppression ^[1]	Carrier frequency range: $50\text{MHz} \leq f \leq 6\text{GHz}$	$\geq 40\text{dBc (typ.)}$
Suppression of image sideband ^[2]	Modulation bandwidth up to $\pm 10\text{MHz}$	$\geq 40\text{dBc (typ.)}$
External I/Q inputs	VSWR	< 1.5
	Full scale input	$\sqrt{I^2 + Q^2} = 0.5V_{rms}$
Internal modulation		
EVM	16QAM, root cosine filter ($\alpha=0.22$), 4MSps	
	$50\text{MHz} \leq f \leq 3\text{GHz}$ (level $\leq 4\text{dBm}$)	$\leq 0.7\%_{rms}$ (typ.)
	$3\text{GHz} < f \leq 6\text{GHz}$ (level $\leq 0\text{dBm}$)	$\leq 1.2\%_{rms}$ (typ.)
	QPSK, root cosine filter ($\alpha=0.22$), 4MSps	
	$50\text{MHz} \leq f \leq 3\text{GHz}$ (level $\leq 4\text{dBm}$)	$\leq 0.7\%_{rms}$ (typ.)
	$3\text{GHz} < f \leq 6\text{GHz}$ (level $\leq 0\text{dBm}$)	$\leq 1.2\%_{rms}$ (typ.)
External modulation		
EVM	CDMA2000/1xEV-D0,	$\leq 1.2\%$,
	1.2288 Mcps, frequency 800 to 900MHz, 1800 to 1900MHz,	$\leq 0.8\%$ (typ.)
ACPR	level $\leq 4\text{dBm}$	$\geq 70\text{dB}$

NOTE: [1][2] The parameter is measured at room temperature. When the temperature is difference from room temperature, the specification will deteriorate.

I/Q Baseband Generator (Option IQ-DSG3000)			
Output impedance	50 Ω (nom.)		
Output voltage	Setting range	0.1V _{pp} to 1.5V _{pp}	
	Resolution	1mV	
Frequency response	Referenced to 1MHz	$\leq 10\text{MHz}$	<0.5dB (nom.)
		$\leq 30\text{MHz}$	<1dB (nom.)
I/Q imbalance	Magnitude	$\leq 10\text{MHz}$	<0.1dB (nom.)
		$\leq 30\text{MHz}$	<0.2dB (nom.)
	Nonlinear phase	$\leq 10\text{MHz}$	200ps (nom.)
		$\leq 30\text{MHz}$	500ps (nom.)
SFDR	Sine	$\leq 30\text{MHz}$	>50dB (nom.)
Waveform memory	Waveform length	1 sample to 8 Msample in one-sample steps	
	Resolution	14 bits	
	Loading time 1Msample	<10 s ^[1] (nom.)	
	Nonvolatile memory	1G Bytes	
Sample rate	Setting range	1 kHz to 50 MHz, 100 MHz	
	Resolution	0.01 Hz	
Trigger	Triggering	Auto, trigger key, external, bus(GPIB, USB, LAN)	
	Operating modes	Retrig, armed auto, armed retrigger, single	
	External trigger delay		
	Setting range	0 to (2 ¹⁶ - 1)	
	Resolution	1	
	External trigger inhibit		
	Setting range	0 to (2 ¹⁶ - 1)	
	Resolution	1	
	External trigger pulse width	>20 ns (nom.)	

NOTE: [1] Load from flash internal non-volatile memory.

RIGOL

Headquarter

RIGOL TECHNOLOGIES, INC.
No.156,Cai He Village,
Sha He Town,
Chang Ping District, Beijing,
102206 P.R.China
Tel:+86-10-80706688
Fax:+86-10-80705070
Email: info@rigol.com

USA

RIGOL TECHNOLOGIES
USA,INC.
7401 First Place,Suite N
Oakwood Village
OH 44146,USA
Tel/Fax: 440-232-4488
Toll free: 877-4-RIGOL-1
Email: info@rigol.com

Europe

RIGOL TECHNOLOGIES EU,
GmbH
Lindbergh str. 4
82178 Puchheim, Germany
Tel: +49(0)89-8941895-0
Email: info-europe@
rigoltech.com

RIGOL® is the registered trademark of RIGOL Technologies, Inc. Product information in this document subject to update without notice. For the latest information about RIGOL's products, applications and services, please contact local RIGOL office or access RIGOL official website:

www.rigol.com

September,2013